

Please add new claims 11-16 as follows:

--11 A memory device as claimed in claim 2, wherein a common electrode is provided between two layers, an input electrode is provided on one of the layers and an output electrode is provided on the other of the layers, the input and output electrodes being disposed on opposite sides of their respective layers compared with the common electrode.--

--12. A memory device comprising a plurality of devices as claimed in claim 2.--

--13. A memory device comprising a plurality of devices as claimed in claim 3.--

--14. A memory device comprising a plurality of devices as claimed in claim 4.--

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--15. A method as claimed in claim 8, wherein the step of detecting the said resultant voltage comprising the phase of the said resultant voltage with the phase of the said applied non-polarising voltage.--

--16. A method as claimed in claim 9, wherein the step of detecting the said resultant voltage comprising the phase of the said resultant voltage with the phase of the said applied non-polarising voltage.--